



Shri Vile Parle Kelavani Mandal's
DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING
(Autonomous College Affiliated to the University of Mumbai)
NAAC Accredited with "A" Grade (CGPA : 3.18)

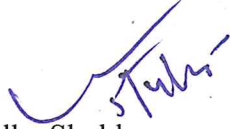


REGULAR EXAMINATION TIMETABLE, MAY, 2025
MASTER OF TECHNOLOGY (M. Tech.) All Branches
FIRST YEAR, SEMESTER – II (For 2024-25 Batch Under Autonomy)
Timings: 02.30 p.m. To 04.30 p.m.

Date: 5th April, 2025

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Computer Engineering
Thursday, 22/05/2025	Additive Manufacturing and Rapid Prototyping	Advanced Wireless Communication Networks	Big Data Infrastructure
Saturday, 24/05/2025	Advanced Quantitative Techniques	IOT & Sensor Networks	Computational Intelligence
Tuesday, 27/05/2025	Industrial Automation	RF and Microwave Engineering	Computational Linguistics
Thursday, 29/05/2025	Project Management		

Important Note: Changes if any, in the timetable, shall be communicated on the College website (djsce.ac.in).


Ms. Alka Shukla
Controller of Examinations


Dr. Hari Vasudevan
Principal