



RE-EXAMINATION TIMETABLE, MARCH, 2025
BACHELOR OF TECHNOLOGY (B.Tech.)
THIRD YEAR, SEMESTER-V (for 2024-25 Batch Under Autonomy)
 Timings: 9:00 a.m. to 11:00 a.m.

Date: 8th March, 2025

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)	Artificial Intelligence & Machine Learning	Computer Science and Engineering (IOT and Cyber Security with Block Chain Technology)
Friday, 21/03/2025	Industrial Electronics	Microcontroller & Applications - II	-	-	-	-	Vulnerability Assessment and Penetration Testing
Saturday, 22/03/2025	-	Mathematics and Statistics for Artificial Intelligence & Machine Learning	-	Nature Inspired Computing	Econometric Modelling	-	-
Monday, 24/03/2025	Data Analytics	-	-	-	-	-	-
	Incubation, Entrepreneurship and Start-ups						
Wednesday, 26/03/2025	Automotive Prime Movers	Radio Frequency Circuit Design	-	Advanced Database Management System	Time Series Analysis	-	Introduction to Blockchain Technology
Thursday, 27/03/2025	Dynamics of Machinery	Analog Communication	-	Artificial Intelligence	Intelligent Systems	-	Artificial Intelligence
Friday, 28/03/2025	Heat Transfer	Digital Signal Processing	Statistical Analysis	Formal Language and Automata Theory	Image Processing and Computer Vision - I	Digital Signal and Image Processing	Network Security

Important Note: Change if any, in the time table shall be communicated on the College website (www.djsce.ac.in).

Ms. Alka Shukla
 Controller of Examinations

Dr. Hari Vasudevan
 Principal