



**RE-EXAMINATION TIMETABLE, MARCH, 2025**  
**BACHELOR OF TECHNOLOGY (B. Tech.) All Branches**  
**FIRST YEAR, SEMESTER – I (for 2023-24 to 2024-25 Batch Under Autonomy)**  
**Timings: 9.00 a.m. to 11.00 a.m.**


Date: 8<sup>th</sup> March, 2025


Day & Date	Group "A"	Group "B"
Friday, 21/03/2025	Basic Electrical Engineering and Digital Electronics	--
Saturday, 22/03/2025	Structured Programming using C	Structured Programming using C
Monday, 24/03/2025	Computational Engineering Mechanics	--
Tuesday, 25/03/2025	--	Chemistry
Wednesday, 26/03/2025	Physics	Effective Communication Skills
Friday, 28/03/2025	Mathematics- I	Mathematics- I

**Group "A":** Electronics & Telecommunication Engineering, Mechanical Engineering, Artificial Intelligence & Machine Learning, Artificial Intelligence & Data Science, Computer Science and Engineering (IOT and Cyber Security with Block Chain Technology)

**Group "B":** Computer Engineering, Information Technology, Computer Science & Engineering (Data Science)

**Important Note:** Changes if any, in the timetable, shall be communicated on the College website ([www.djsce.ac.in](http://www.djsce.ac.in)).

  
Ms. Alka Shukla  
Controller of Examinations

  
Dr. Hari Vasudevan  
Principal