

Name of Teaching Staff : Prof.(Mrs.) Poonam A. Kadam
Designation : Assistant Professor
Department : Electronics & Telecommunication Engineering
Date of Joining the Institution : 2.7.2007



Qualifications with Class / Grade : 1. M.Tech (Microelectronics) from Banaras Hindu University with 1st Class Distinction in June 2003 CGPA 9.13.
2. B.E. (Electronics Engineering) in May/June 2001 from Swami Ramanand Tirth Marathwada University with 76.30%, 1st Class Distinction.

Total Experience in Years : **Teaching: 16 years**
1. Assistant Professor in EXTC Dept at D. J. Sanghvi College of Engineering from 2.7.2007 till date.
2. Lecturer in I.T., D. J. Sanghvi College of Engineering from 3.2.2006 to 30.06.2006 and from 11.7.2006 to 01.7.2007.
3. Lecturer at S.G.G.S Institute of Technology from 3.2.2003 to Jan 2006.

Industry: --

Research: --

1. Poonam Varma, V. Shahane, "Efficient VLSI computing paradigm using a Novel 5*5 Universal Reversible gate", DJSCOE & NMIMS – National conference on information and communication Technology New Horizon in Technology and Applications, PP.4, March 01-03, 2007.
2. Poonam Kadam, Aksheit J, Bhavin M, Ronak H, "Multiplier decoupling SCRL technique" – NCCT 2011.
3. FPGA implementation of AES algorithm using VHDL, Technofocus.

International: 12

1. Amit Deshmukh, Priyal zaveri, Poonam Kadam, "On the Design of Circularly Polarized U-slot Cut Square Microstrip Antenna" IEEE, **CSCITA 2017**.
2. Amit Deshmukh, Adil Parvez, Priyanka Verma, Ami Desai, Poonam Kadam and Kamala Prasan Ray, "Space Fed Ring Microstrip Antenna array with Stacked Rectangular Microstrip Antenna Feed", **INDICON 2016**.
3. Poonam Kadam, Kapil Gavali, "VLSI design of high speed Vedic multiplier for FPGA implementation", in 2016 IEEE conference on Engineering and Technology, 17th and 18th March 2016
4. Poonam Kadam, Bhakti Patel "Comparative Analysis of Adiabatic logic techniques", International Journal of Computer Application, (0975-8887), **ICCT 2015**
5. Poonam Kadam, Nilima Parmar, "Combined Architecture for AES Encryption and Decryption using FPGA", Communication on Applied Electronics- ISSN: 2394- 4714, **ICCT 2015**
6. Poonam Kadam, Nilima Parmar, "High speed architecture implementation of AES using FPGA", international Journal of Computer Application, (0975-8887), **ICCT 2015**
7. Poonam Kadam, Aruna Rani "Split level charge recovery logic" Proceedings of National Conference on VLSI & Image Processing, NCVLSI'13, March 2013.
8. A. A. Deshmukh, Shafin Nagarbowdi, P. A. Kadam, A.A. Odhekar, "Broadband Gap-coupled Isosceles Triangular Microstrip Antennas, 2017 International Conference on Emerging Trends & Innovation in ICT (ICEI) Pune Institute of Computer Technology, Pune, India, Feb 3-5, 2017.

9.A. A. Deshmukh, P. A. Kadam , Shefali Pawar, K P Ray,” Analysis of Single Shorted Square Microstrip Antenna”, International Conference on Emerging Trends & Innovation in ICT (ICEI) Pune Institute of Computer Technology, Pune, India, Feb 3-5, 2017.

10.A. A. Deshmukh, P. A. Kadam , Darshan Gala, K P Ray,” Multi-band Square Microstrip Antenna Using Defected Ground Plane”, IEEE, ICCUBEA2017.

11.Amit A. Deshmukh, Poonam Kadam, Akshay Doshi, Darshan Gala,”Wide band Designs of Rectangular Microstrip Antenna Using modified ground plane”, IEEE, ICAC2017.

12. A. A. Deshmukh, P. A. Kadam, A. Odhekar, “ Air suspended Multiband E- shaped Microstrip Antenna” IEEE, ICCUBEA2018.

Papers Presented in
Conferences

: **National:** 2

1. Poonam Varma, V. Shahane, “Efficient VLSI computing paradigm using a Novel 5*5 Universal Reversible gate”, DJSCOE & NMIMS – National conference on information and communication Technology New Horizon in Technology and Applications, PP.4, March 01-03, 2007.
2. Poonam Kadam, Aksheit J, Bhavin M, Ronak H, “Multiplier decoupling SCRL technique” – NCCT 2011.

International: 7

1.Amit Deshmukh, Priyal zaveri, Poonam Kadam, On the Design of Circularly Polarized U-slot Cut Square Microstrip Antenna”IEEE, CSCITA 2017.

2. Amit Deshmukh, Adil Parvez, Priyanka Verma, Ami Desai, Poonam Kadam and Kamala Prasan Ray, Space Fed Ring Microstrip Antenna array with Stacked Rectangular Microstrip Antenna Feed”, INDICON 2016.

3.Amit A. Deshmukh, Poonam Kadam, Akshay Doshi, Darshan Gala,”Wide band Designs of Rectangular Microstrip Antenna Using modified ground plane”, IEEE, ICAC 2017.

4. A. A. Deshmukh, P. A. Kadam, A. Odhekar, “ Air suspended Multiband E- shaped Microstrip Antenna” IEEE, ICCUBEA 2018.

PhD Guide ? Give field & University : **Field:** --
University: --

PhDs / Projects Guided : **PhDs :** --

Projects at Masters level: 05

Books Published / IPRs / Patents : 1. VLSI Design (MU) Techmax Publication.
2. Advanced VLSI Design (MU) Techmax Publication.
3. VLSI Design and Technology (GTU) Techmax Publication.
4. VLSI Design and Technology, Pune University, Techmax Publication.

Professional Memberships : **IETE member**

Consultancy Activities : --

Awards : --

Grants fetched : --

Interaction with Professional Institutions : --